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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Technical Talk

On

"Latest Development & Career Opportunities in VLSI"

29/10/22

Resource Person/s

Mr. Anikethan H.V.U
Co-founder
CoachEd
Bangalore

Mr. Arun Iyer
Principal Engineer
Xilinx AMD
Hyderabad

GM INSTITUTE OF TECHNOLOGY, DAVANGERE

Deepika.V.B

Event coordinators
Mr. Santhosh B G/ Ms. Deepika V B
Assistant Professor

Pls. add objective & Outcome of the event-

Checked by
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Dean Academics
18/11/22

Verified By
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Head of the Department
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Approved by
Dr. Y. Vijay Kumar
Principal

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Report On "Latest Development & Career Opportunities in VLSI"

The Department of Electronics and Communication Engineering, GM Institute of Technology, Davangere organized a Technical talk on **"Latest Development & Career Opportunities in VLSI"** by the resource persons **Mr. Arun Iyer,** Principal Engineer, Xilinx AMD, Hyderabad & **Mr. Anikethan H.V.U**, Co-founder, Partnerships and Operations at CoachEd, Bengaluru, on 29th October 2022 at 10.30 am in Central Library Auditorium.

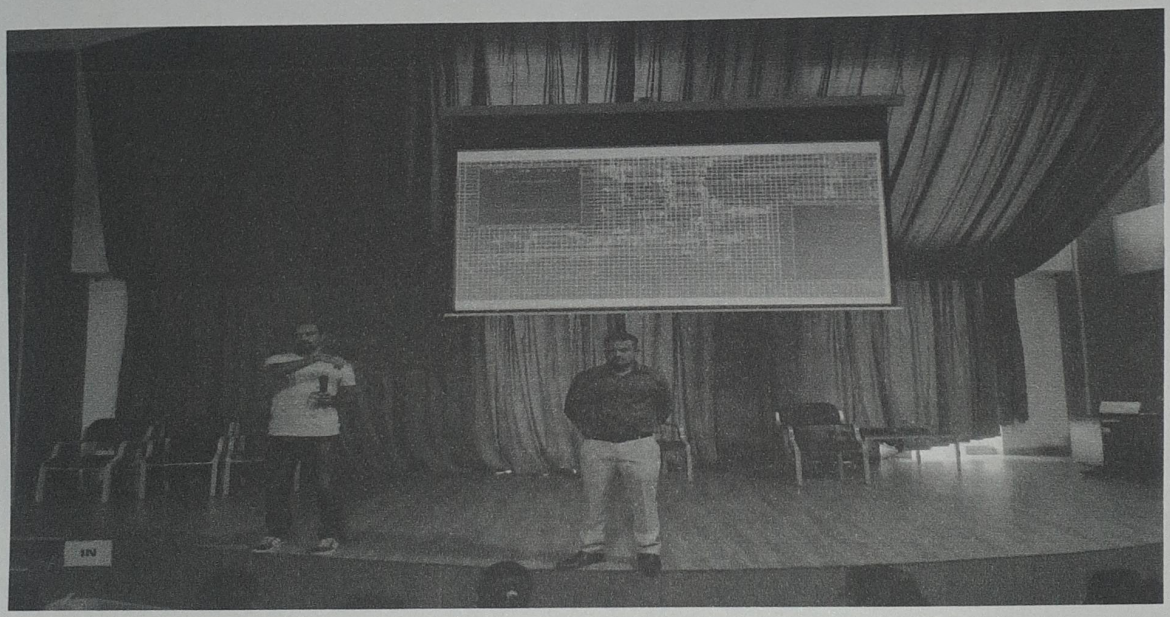
Dr. Y Vijaya Kumar, Principal was presided over the function. **Dr.Praveen J**, Professor & Head, ECE department, and **Prof. Tejasvi Kattimani**, Training & Placement Officer were also present.

The faculty members of ECE and the students of ECE were present in the seminar and Dr. Praveen J, Convener & Head, ECE department addressed the students about the importance of the seminar topic and Career Opportunities in VLSI research field. Dr. Kavitha K J, Associate Professor, ECE department carried out the Program by welcoming the gathering and introduced one of the chief guests **Mr. Anikethan H.V.U**, Co-founder, Partnerships and Operations at CoachEd, Bengaluru and Mr. Santhosh.B.G, Assistant Professor, ECE department gave introduction about another key note speaker **Mr. Arun Iyer**, Principal Engineer, Xilinx AMD, Hyderabad to the audience.

The key note speaker of the seminar Mr. ArunIyer, started the presentation by giving introduction about Latest Development & Career Opportunities in VLSI. He also gave awareness and

importance of VLSI technology, explained the types of Design Systems, its design flow and its wide area of applications in various field.

- In Design flow there will be Front end design and Back end design.
- Front end design specifications are either external clients or internal marketing team and Power-Performance-Area (PPA)
- In Front end, architecture design is Based on specifications, different blocks like processor, memory ,etc., and these IPs from third party vendors(say, a processor core in SoC) or design from scratch and it is Low power architecture with physical security.
- RTL (Register Transfer Level) Design architecture is a Digital design that is Combinational & sequential design with VHDL or verilog and describes data operations during transfer between two registers and Shift towards HLS(High Level Synthesis) in recent times - uses languages like C,C++,SystemC on platforms like OpenCL. It includes Gate-level verification and DFT(Design For Testing).
- Back end design specifications are Partitioning and Floor-planning, Placement, CTS, Routing, Signoff, Fabrication, Packaging and testing and chip production.



The wide application of VLSI's Popular system is the CMOS technology which can perform multiple functions by deploying a single chip. Also, the technology is deployed in computer systems,

modern electronic appliances, automobiles, etc. were also explained by the key note speakers in the seminar

The resource persons encouraged all the faculty members and students by giving awareness about importance of CMOS VLSI Design in research field in both Academics and Industry to build their Career. The faculty members and students of ECE are motivated and benefited.

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